

PRODUCT CHARACTERISTICS

V_{DSS}	700V
$R_{DS(ON)}$ Typ(@ $V_{GS}=10V$)	0.71 Ω
Qg@typ	38.6nC
I_D	12A

APPLICATIONS

- * High efficiency switch mode power supplies
- * Electronic lamp ballasts based on half bridge
- * LED power supplies

FEATURE

- * High Switching Speed
- * Improved dv/dt capability

ORDER INFORMATION

Order Codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT12N70F	TO-220F	50 pieces/Tube

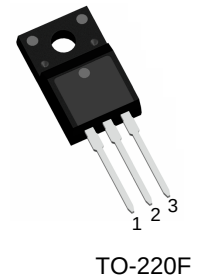
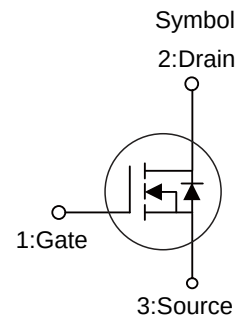
ABSOLUTE MAXIMUM RATINGS($T_A=25^{\circ}C$, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DSS}	700	V
Gate-Source Voltage	V_{GSS}	± 30	V
Drain Current Continuous(@ $V_{GS}=10V$, $T_A=25^{\circ}C$)	I_D	12	A
Drain Current Pulsed	I_{DM}	48	A
Avalanche Energy *	E_{AS}	845	mJ
Peak Diode Recovery dv/dt	dv/dt	5.0	V/ns
Power Dissipation	P_D	42	W
Junction Temperature	T_J	+150	$^{\circ}C$
Storage Temperature	T_{STG}	-55~ +150	$^{\circ}C$

THERMAL CHARACTERISTICS

Parameter	Symbol	Typ	Unit
Junction to Ambient	R_{thJA}	62.5	$^{\circ}C/W$
Junction to Case	R_{thJC}	2.98	$^{\circ}C/W$

Note: * EAS condition: $T_J=25^{\circ}C$, $V_{DD}=50V$, $V_G=10V$, $L=10mH$, $R_g=25\Omega$



■ ELECTRICAL CHARACTERISTICS ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off characteristics						
Drain to Source Breakdown Voltage	V _{DSS}	V _{GS} =0V, I _D =250μA	700	-	-	V
Drain to Source Leakage Current	I _{DSS}	V _{DS} =700V, V _{GS} =0V	-	-	1	μA
Gate to Source Forward Leakage	I _{GSS(F)}	V _{DS} =0V, V _{GS} =+30V	-	-	100	nA
Gate to Source Reverse Leakage	I _{GSS(R)}	V _{DS} =0V, V _{GS} =-30V	-	-	-100	nA
On characteristics						
Drain to Source On-Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =6A	-	0.71	0.85	Ω
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2	-	4	V
Dynamic characteristics						
Gate capacitance	R _g	V _{GS} =0V, V _{DS} =0V ,f=1.0MHz	-	3	-	Ω
Forward Transconductance	g _{fs}	V _{DS} =10V, I _D =3A	-	7	-	S
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V f=1.0MHz	-	2200	-	pF
Output Capacitance	C _{oss}		-	133	-	pF
Reverse Transfer Capacitance	C _{rss}		-	2	-	pF
Resistive Switching Characteristics						
Turn-on Delay Time	t _{d(ON)}	I _D =12A, V _{DS} =350V R _G =10Ω ,V _{GS} =10V	-	28	-	ns
Rise Time	t _r		-	26	-	ns
Turn-off Delay Time	t _{d(OFF)}		-	64	-	ns
Fall Time	t _f		-	45	-	ns
Total Gate Charge	Q _g	I _D =12 A, V _{DS} =350V V _{GS} =10V	-	38.6	-	nC
Gate to Source Charge	Q _{gs}		-	9.2	-	nC
Gate to Drain("Miller") Charge	Q _{gd}		-	16	-	nC
Source-Drain Diode Characteristics						
Continuous Source Current(Body Diode)	I _S		-	-	12	A
Maximum Pulsed Current(Body Diode)	I _{SM}		-	-	48	A
Diode Forward Voltage	V _{SD}	I _{SD} =1A, V _{GS} =0V	-	0.71	1.2	V
Reverse Recovery Time	t _{rr}	I _{SD} =12A, T _J =25 °C	-	536	-	ns
Reverse Recovery Charge	Q _{rr}	dI/dt=100A/μs	-	4693	-	nC

■ TYPICAL CHARACTERISTICS

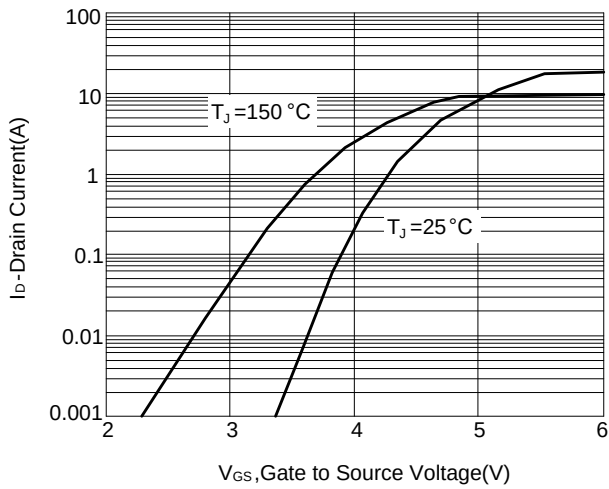


Figure 1: Transfer Characteristics

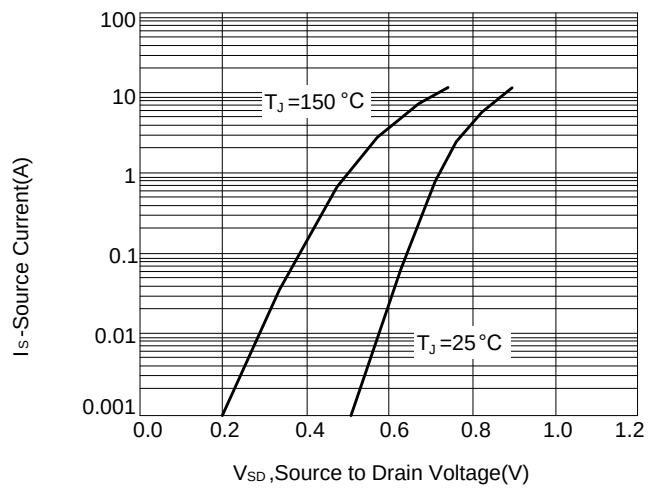


Figure 2: Body Diode Characteristics

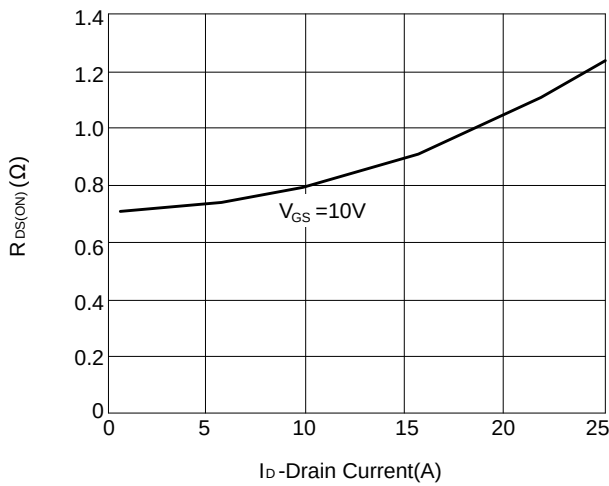


Figure 3: Drain to Source On-Resistance vs Drain Current

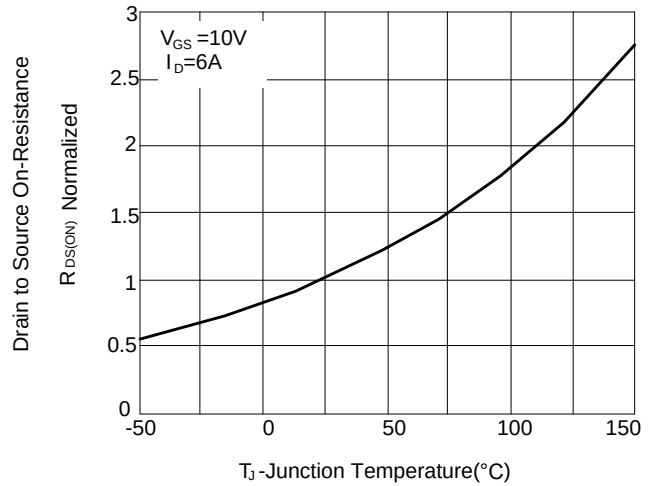


Figure 4: Drain to Source On-Resistance vs Junction Temperature

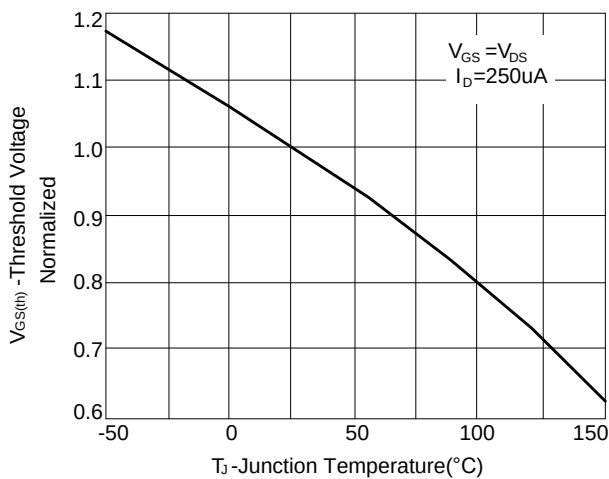


Figure 5: Threshold Voltage vs Junction Temperature

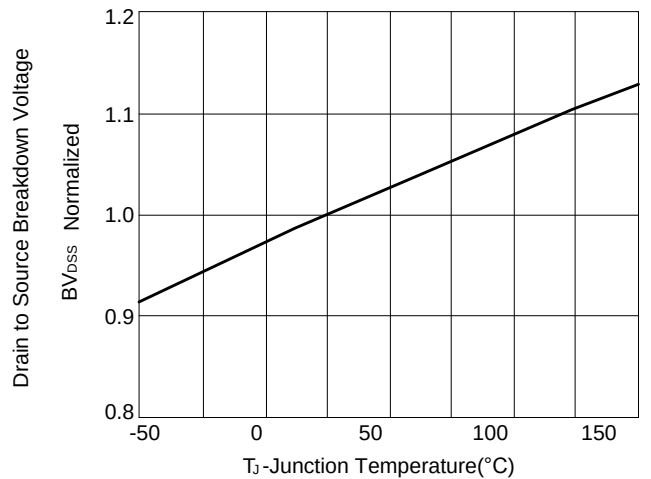


Figure 6: Breakdown Voltage vs Junction Temperature

■ TYPICAL CHARACTERISTICS(Cont.)

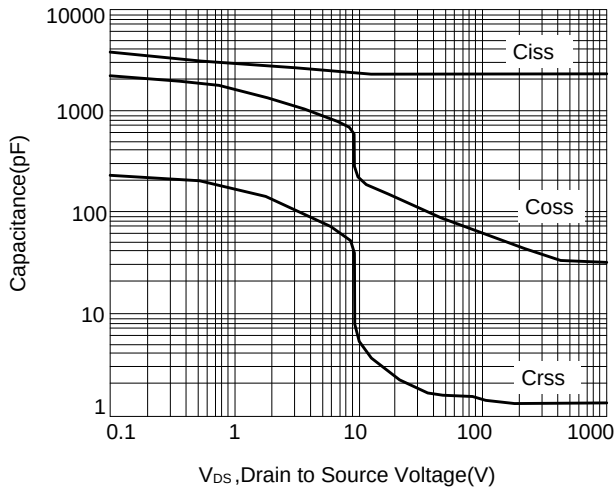


Figure 7: Capacitance vs Drain to Source Voltage

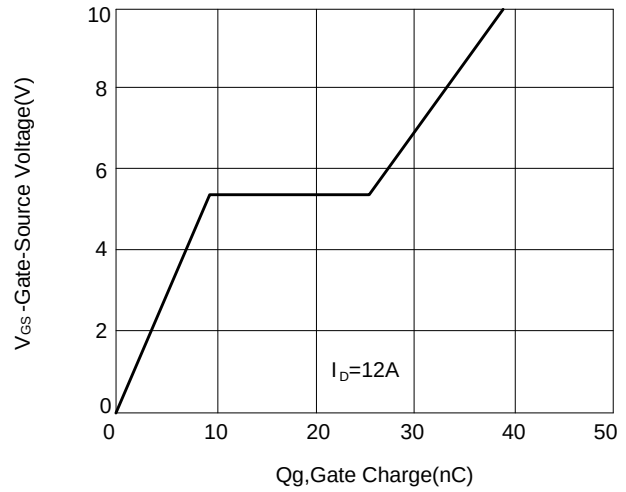


Figure 8: Gate Charge vs Gate to Source Voltage

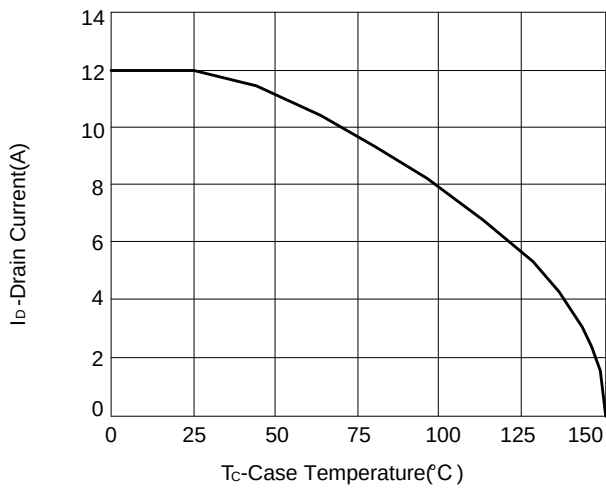


Figure 9: Continuous Drain vs Case Temperature

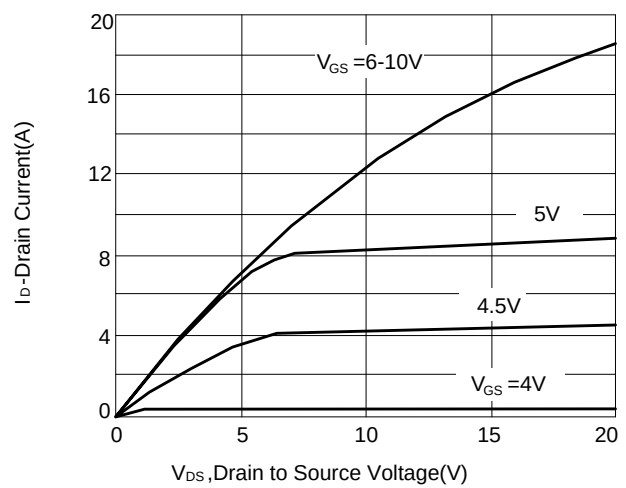


Figure 10: Output Characteristics

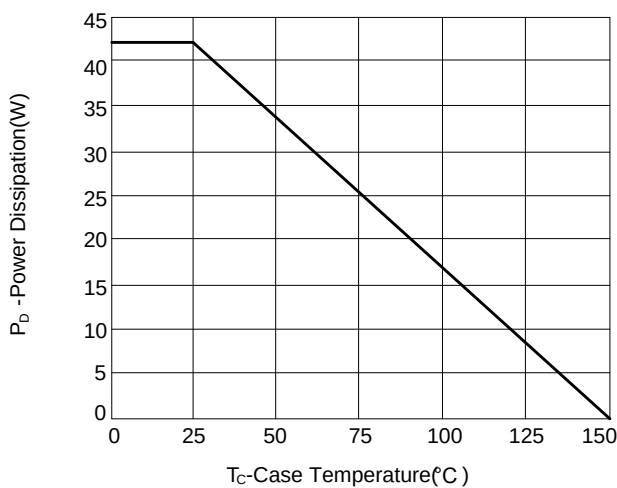


Figure 11: Power Dissipation vs Case Temperature

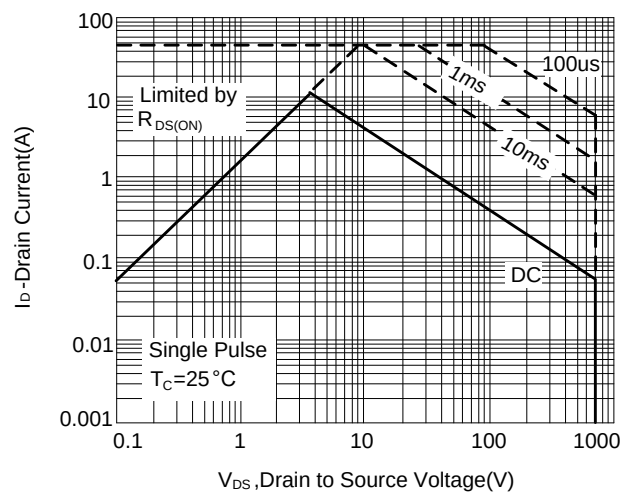


Figure 12: Safe operating Area

■ TO-220F PACKAGE OUTLINE DIMENSIONS

