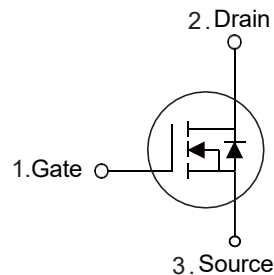


PRODUCT CHARACTERISTICS

V_{DSS}	60V
$R_{DS(ON) Typ(@V_{GS}=10V)}$	8.6m Ω
$R_{DS(ON) Typ(@V_{GS}=4.5V)}$	11m Ω
I_D	55A

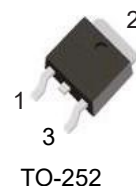


APPLICATIONS

- * Electronic lamp ballasts based on half bridge
- * Load Switching, Quick/Wireless Charge.
- * Motor Driving

FEATURE

- * Low Gate Charge
- * Pb-Free Lead Plating



ORDER INFORMATION

Order Codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT6512D	TO-252	2500 pieces/Reel

ABSOLUTE MAXIMUM RATINGS($T_A=25^{\circ}C$, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DSS}	60	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current Continuous($@V_{GS}=10V, T_A=25^{\circ}C$)	I_D	55	A
Drain Current Continuous($@V_{GS}=10V, T_A=100^{\circ}C$)	I_D	33	A
Drain Current Pulsed	I_{DM}	220	A
Avalanche Energy *	E_{AS}	156	mJ
Power Dissipation	P_D	48	W
Junction Temperature	T_J	+150	$^{\circ}C$
Storage Temperature	T_{STG}	-55~ +150	$^{\circ}C$

THERMAL CHARACTERISTICS

Parameter	Symbol	Typ	Unit
Junction to Case	R_{thJC}	2.6	$^{\circ}C/W$

Note: * EAS condition: $T_J=25^{\circ}C, V_{DD}=30V, V_G=10V, L=0.5mH, R_g=25\Omega$

■ ELECTRICAL CHARACTERISTICS ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off characteristics						
Drain to Source Breakdown Voltage	V _{DSS}	V _{GS} =0V,I _D =250μA	60	-	-	V
Drain to Source Leakage Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate to Source Forward Leakage	I _{GSS(F)}	V _{GS} =+20V,V _{DS} =0V	-	-	100	nA
Gate to Source Reverse Leakage	I _{GSS(R)}	V _{GS} =-20V ,V _{DS} =0V	-	-	-100	nA
On characteristics						
Drain to Source On-Resistance	R _{DS(ON)}	V _{GS} =10V,I _D =20 A	-	8.6	10	mΩ
		V _{GS} =4.5V,I _D =15 A	-	11	13	mΩ
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} ,I _D =250μA	1	1.4	2.5	V
Dynamic characteristics						
Gate Resistance	R _g	V _{GS} =0V,V _{DS} =0V ,f=1.0MHz	-	1.7	-	Ω
Forward Transconductance	g _{fs}	V _{DS} =5V,I _D =5A	-	16	-	S
Input Capacitance	C _{iss}	V _{DS} =20V,V _{GS} =0V f=1.0MHz	-	2400	-	pF
Output Capacitance	C _{oss}		-	172	-	pF
Reverse Transfer Capacitance	C _{rss}		-	140	-	pF
Resistive Switching Characteristics						
Turn-on Delay Time	t _{d(ON)}	V _{GS} =10V, V _{DS} =30V, I _D =30 A ,R _G =3Ω	-	11	-	ns
Rise Time	t _r		-	32	-	ns
Turn-off Delay Time	t _{d(OFF)}		-	45	-	ns
Fall Time	t _f		-	10	-	ns
Total Gate Charge	Q _g	I _D =30 A, V _{DS} =30V V _{GS} =10V	-	47	-	nC
Gate to Source Charge	Q _{gs}		-	13	-	nC
Gate to Drain("Miller") Charge	Q _{gd}		-	8	-	nC
Source-Drain Diode Characteristics						
Continuous Source Current(Body Diode)	I _s		-	-	55	A
Maximum Pulsed Current(Body Diode)	I _{SM}		-	-	220	A
Diode Forward Voltage	V _{SD}	I _{SD} =1A, V _{GS} =0V	-	0.73	1.2	V
Reverse Recovery Time	t _{rr}	I _{SD} =30 A, T _j =25 °C	-	27	-	ns
Reverse Recovery Charge	Q _{rr}	di /dt=100A/μs	-	40	-	nC

■ TYPICAL CHARACTERISTICS

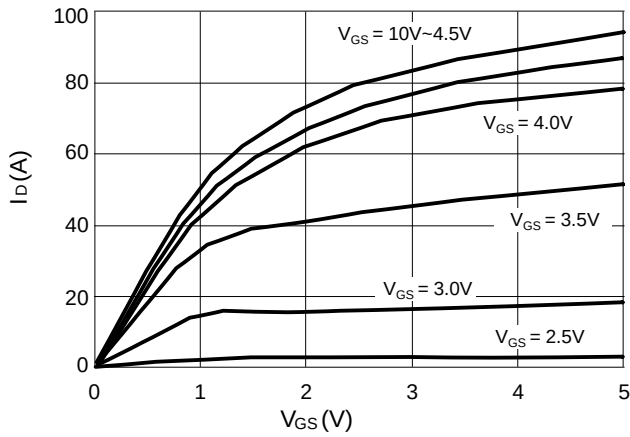


Figure 1: Output characteristics

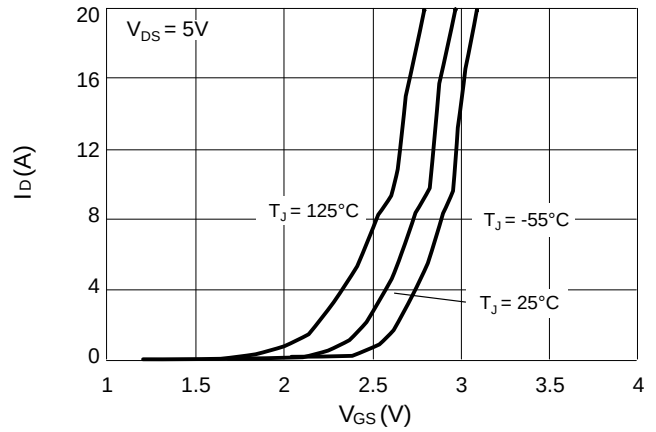


Figure 2: Transfer characteristics

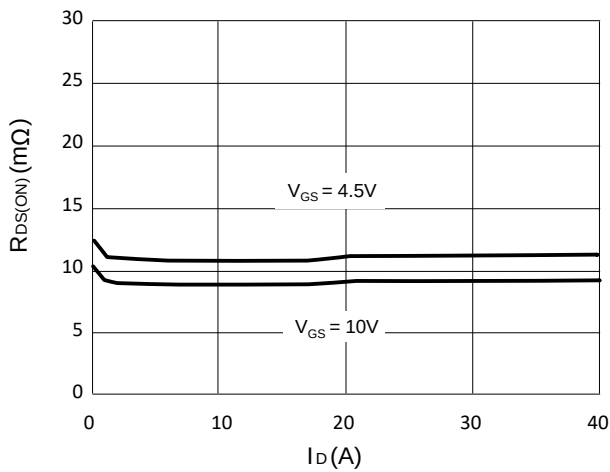


Figure 3: On-resistance vs. drain current

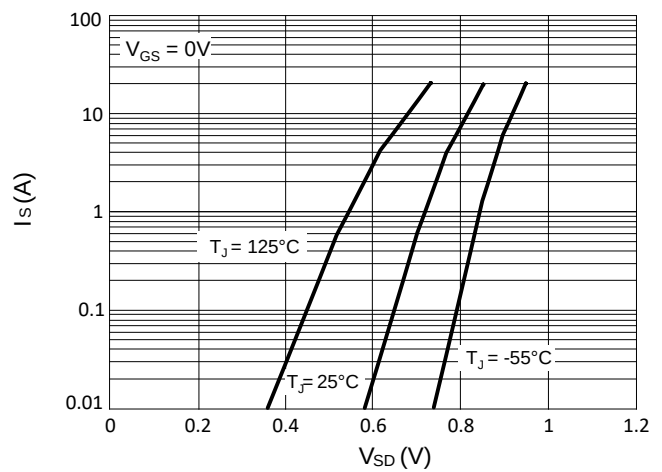


Figure 4: Body diode characteristics

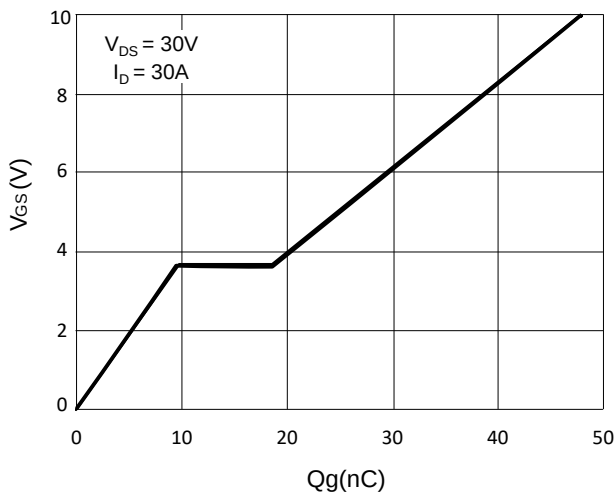


Figure 5: Gate charge characteristics

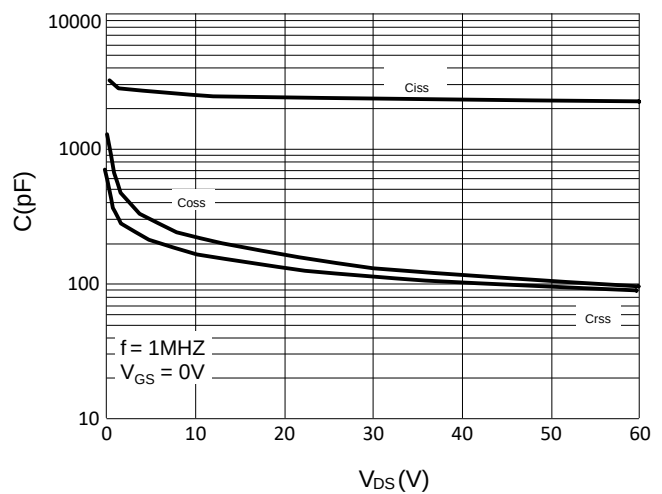


Figure 6: Capacitance characteristics

■ TYPICAL CHARACTERISTICS(Cont.)

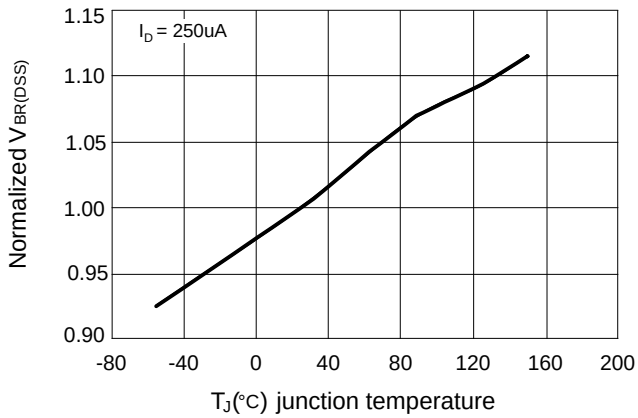


Figure 7: Normalized breakdown voltage vs. junction temperature

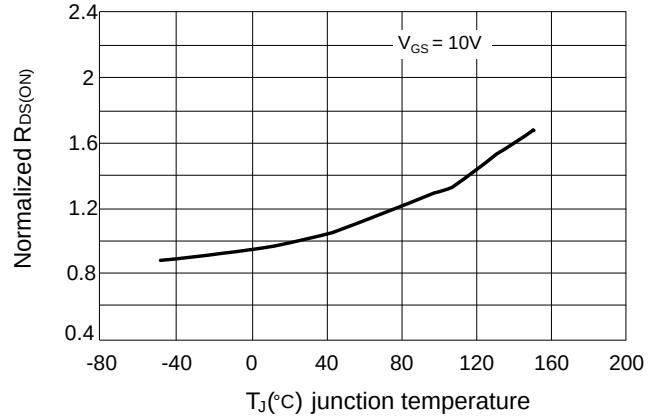


Figure 8: Normalized on-resistance vs. junction temperature

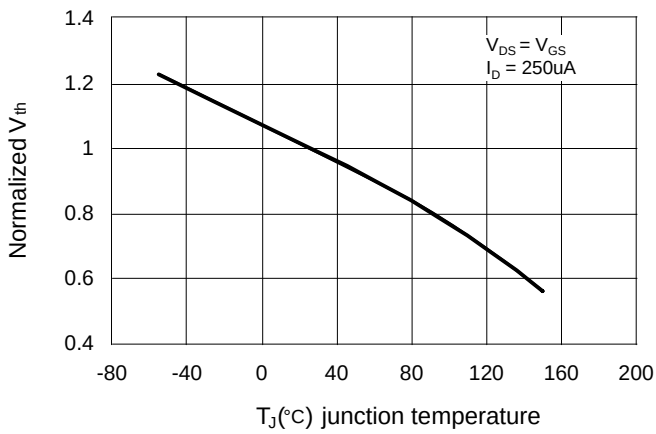


Figure 9: Normalized threshold voltage vs. junction temperature

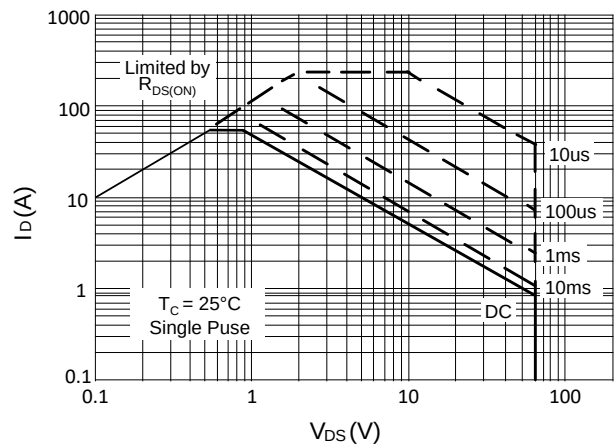


Figure 10: Maximum sffe operating area

■ TO-252 PACKAGE OUTLINE DIMENSIONS

